



Attorney Docket No. YOR920030428US1

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Patent Application**

Applicant(s): Brian A. Floyd  
Docket No.: YOR920030428US1  
Serial No.: 10/671,984  
Filing Date: September 26, 2003  
Group: 2817  
Examiner: Henry Choe

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature: *[Handwritten Signature]* Date: March 22, 2005

Title: Bypass Switch Topology for Low-Noise Amplifiers

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**INFORMATION DISCLOSURE STATEMENT**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicants' attorney wishes to bring to the attention of the Patent and Trademark Office the following documents listed on the accompanying Form PTO-1449. Copies of the listed documents are enclosed together with the search report that listed these documents, other than published U.S. patent documents.

**U.S. Patent Documents**

1. U.S. Patent Publication No. US 2003/0112065 issued on 06/19/03 to Brown et al.
2. U.S. Patent Publication No. US 2003/0058041 issued on 03/27/03 to Koizumi
3. U.S. Patent No. 6,351,183 issued on 02/26/02 to Khabbaz et al.

**Other Documents**

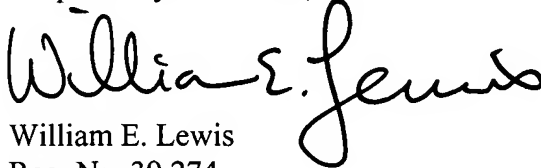
1. H. Morkner et al., "A 1.7mA Low Noise Amplifier with Integrated Bypass Switch for Wireless 0.05-6 GHz Portable Applications," XP-001067282, IEEE MTT-S Digest, pp. 293-296, 2001.

2. T. Nakatani et al., "A Wide Dynamic Range Switched-LNA in Sige BICMOS," XP-001067279, IEEE MTT-S Digest, pp. 281-284, 2001.

The undersigned certifies in accordance with 37 C.F.R. §1.97(e)(1) that each item listed above was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. It is believed that there is no fee due in conjunction with the filing of this Information Disclosure Statement. In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **International Business Machines Corporation Deposit Account No. 50-0510** as required to correct the error.

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability, or as a representation that no other material information exists.

Respectfully submitted,

A handwritten signature in black ink, reading "William E. Lewis". The signature is fluid and cursive, with the first name "William" and last name "Lewis" clearly legible.

William E. Lewis  
Reg. No. 39,274  
Attorney for Applicant(s)  
Ryan, Mason & Lewis, LLP  
90 Forest Avenue  
Locust Valley, NY 11560  
(516) 759-2946

Date: March 22, 2005

## FORM PTO-1449 (MODIFIED)

LIST OF PUBLICATIONS FOR  
APPLICANT'S INFORMATION  
DISCLOSURE STATEMENT

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## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS/SUBCLASS	FILING DATE IF APPROPRIATE
—	1. 2003/0112065	06/19/03	Brown et al.		
—	2. 2003/0058041	03/27/03	Koizumi		
—	3. 6,351,183	02/26/02	Khabbaz et al.		

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS	TRANSLATION YES NO	
—						

## OTHER DOCUMENTS

EXAMINER INITIAL	REF NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
—	1.	H. Morkner et al., "A 1.7mA Low Noise Amplifier with Integrated Bypass Switch for Wireless 0.05-6 GHz Portable Applications," XP-001067282, IEEE MTT-S Digest, pp. 293-296, 2001.
—	2.	T. Nakatani et al., "A Wide Dynamic Range Switched-LNA in Sige BICMOS," XP-001067279, IEEE MTT-S Digest, pp. 281-284, 2001.

Examiner

Date Considered

**Examiner:** Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.